Notice of Allowability	Application No.	Applicant(s)
	10/795,864	SEBERGER, STEPHEN GEORGE
	Examiner	Art Unit
	J. Casimer Jacyna	3751
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>5/12/2005</u> .		
2. The allowed claim(s) is/are <u>1-30</u> .		
3. The drawings filed on <u>08 March 2004</u> are accepted by the Examiner.		
4.		
 Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/0 Paper No./Mail Date 05122005 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material 	6. ☐ Interview Summary Paper No./Mail Dat 8), 7. ☐ Examiner's Amendm	e

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1. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. The references cited on the PTO-892 teach other controlled valves with limit switches.

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2. The following is an examiner's statement of reasons for allowance: The prior art of record does not teach a limit switch that is usable in a digital valve positioning system that includes the specific structure of a position sensor that indicates the position of the valve, a memory for the limit switch separate from a valve controller memory, a microprocessor for the limit switch that is capable of linking to a separate microprocessor for the valve controller and is coupled to the limit switch memory, wherein the limit switch microprocessor is responsive to the position sensor and set point data stored in the limit switch memory to generate a limit switch signal output as claimed.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to J. Casimer Jacyna whose telephone number is 571-272-4889. The examiner can normally be reached on Wed. thru Fri. 9AM-7PM, Mon. 7AM-1PM.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Justine Yu can be reached on 703-272-4835. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

J. Casimer Jácyría Primary Examiner Art Unit 3751